

FORM PTO-1449 (REV. 7-80)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. T&A-122	SERIAL NO. 10/701511	
LIST OF DOCUMENTS CITED BY APPLICANT <i>(Use several sheets if necessary)</i>				APPLICANT R. TAKEMURA et al FILING DATE November 6, 2003		
				GROUP 2827		
<b>U.S. PATENT DOCUMENTS</b>						
* EXAMINER INITIAL	DOCUMENT	DATE	NAME	CLASS	SUBCLASS	FILING DATE (if Appropriate)
	AA					
	AB					
	AC					
	AD					
	AE					
	AF					
	AG					
	AH					
	AI					
	AJ					
	AK					
<b>FOREIGN PATENT DOCUMENTS</b>						
	DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
TL	AL 2001-256782	09/21/01	Japan (w/English Abstract)			<input type="checkbox"/> <input type="checkbox"/>
	AM					<input type="checkbox"/> <input type="checkbox"/>
	AN					<input type="checkbox"/> <input type="checkbox"/>
	AO					<input type="checkbox"/> <input type="checkbox"/>
	AP					<input type="checkbox"/> <input type="checkbox"/>
<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)</b>						
TL	AR	"WP 24.1 An 8ns Random Cycle Embedded RAM Macro with Dual-Port Interleaved DRAM Architecture (D <sup>2</sup> RAM)", 2000 IEEE International Solid-State Circuits Conference, 07803-5853-8/00.				
	AS					
	AT					
EXAMINER Thoyke			DATE CONSIDERED 3/9/05			

\* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.